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» Key

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IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

1. **Branch predictor prediction: a power-aware branch predictor for high-performance processor**
 Baniasadi, A.; Moshovos, A.;
Computer Design: VLSI in Computers and Processors, 2002. Proceedings., 2002 IEEE International Conference on, 16-18 Sept. 2002 Page(s):458 - 461
 Digital Object Identifier 10.1109/ICCD.2002.1106813
[AbstractPlus](#) | Full Text: [PDF\(341 KB\)](#) IEEE CNF
[Rights and Permissions](#)
2. **Design optimization for high-speed per-address two-level branch predictors**
 Chen, I.-C.K.; Chih-Chieh Lee; Postiff, M.; Mudge, T.;
Computer Design: VLSI in Computers and Processors, 1997. ICCD '97. Proceedings., 1997 IEEE International Conference on, 12-15 Oct. 1997 Page(s):88 - 96
 Digital Object Identifier 10.1109/ICCD.1997.628854
[AbstractPlus](#) | Full Text: [PDF\(1108 KB\)](#) IEEE CNF
[Rights and Permissions](#)
3. **Accurate indirect branch prediction**
 Driesen, K.; Holzle, U.;
Computer Architecture, 1998. Proceedings. The 25th Annual International Symposium on, 27 June-1 July 1998 Page(s):167 - 178
 Digital Object Identifier 10.1109/ISCA.1998.694772
[AbstractPlus](#) | Full Text: [PDF\(108 KB\)](#) IEEE CNF
[Rights and Permissions](#)
4. **Exploring design space of scalable per-address branch predictors**
 Kongmunvattana, A.; Tiamkaew, E.;
TENCON 2004. 2004 IEEE Region 10 Conference, Volume B, 21-24 Nov. 2004 Page(s):156 - 159 Vol. 2
 Digital Object Identifier 10.1109/TENCON.2004.1414555
[AbstractPlus](#) | Full Text: [PDF\(1848 KB\)](#) IEEE CNF
[Rights and Permissions](#)
5. **The effects of mispredicted-path execution on branch prediction structures**

Jourdan, S.; Tse-Hao Hsing; Stark, J.; Patt, Y.N.;
Parallel Architectures and Compilation Techniques, 1996., Proceedings of the 1996 Conference on
20-23 Oct. 1996 Page(s):58 - 67
Digital Object Identifier 10.1109/PACT.1996.552555
[AbstractPlus](#) | Full Text: [PDF\(964 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- 6. **Evaluating branch prediction using two-level perceptron table**
Ribas, L.V.M.; Goncalves, R.A.L.;
Parallel, Distributed, and Network-Based Processing, 2006, PDP 2006, 14th Euromicro International Conference on
15-17 Feb. 2006 Page(s):4 pp.
Digital Object Identifier 10.1109/PDP.2006.34
[AbstractPlus](#) | Full Text: [PDF\(152 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 7. **Branch history table indexing to prevent pipeline bubbles in wide-issue superscalar processor**
Tse-Yu Yeh; Patt, Y.N.;
Microarchitecture, 1993, Proceedings of the 26th Annual International Symposium on
1-3 Dec. 1993 Page(s):164 - 175
Digital Object Identifier 10.1109/MICRO.1993.282746
[AbstractPlus](#) | Full Text: [PDF\(972 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 8. **Microarchitecture support for reducing branch penalty in a superscalar processor**
Sakamoto, M.; Nunomura, Y.; Yoshida, T.; Shimazu, Y.;
Computer Design: VLSI in Computers and Processors, 1996, ICCD '96, Proceedings, 1996 IEEE
7-9 Oct. 1996 Page(s):208 - 216
Digital Object Identifier 10.1109/ICCD.1996.563559
[AbstractPlus](#) | Full Text: [PDF\(776 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 9. **Influence of high-level program structures on branch prediction accuracy**
Ganjoo, A.; Nian-Feng Tzeng;
Euromicro Conference, 2000, Proceedings of the 26th
Volume 1, 5-7 Sept. 2000 Page(s):316 - 322 vol.1
Digital Object Identifier 10.1109/EURMIC.2000.874648
[AbstractPlus](#) | Full Text: [PDF\(540 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 10. **DStride: data-cache miss-address-based stride prefetching scheme for multimedia processor**
Hariprakash, G.; Achutharaman, R.; Omondi, A.R.;
Computer Systems Architecture Conference, 2001, ACSAC 2001, Proceedings, 6th Australasian
29-30 Jan. 2001 Page(s):62 - 70
Digital Object Identifier 10.1109/ACAC.2001.903360
[AbstractPlus](#) | Full Text: [PDF\(756 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 11. **Branch prediction and simultaneous multithreading**
Hily, S.; Seznec, A.;
Parallel Architectures and Compilation Techniques, 1996., Proceedings of the 1996 Conference on
20-23 Oct. 1996 Page(s):169 - 173
Digital Object Identifier 10.1109/PACT.1996.552664
[AbstractPlus](#) | Full Text: [PDF\(556 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 12. **TCP: tag correlating prefetchers**
Hu, Z.; Martonosi, M.; Kaxiras, S.;
High-Performance Computer Architecture, 2003, HPCA-9, 2003, Proceedings, The Ninth International
8-12 Feb. 2003 Page(s):317 - 326

Digital Object Identifier 10.1109/HPCA.2003.1183549

[AbstractPlus](#) | Full Text: [PDF\(348 KB\)](#) IEEE CNF

[Rights and Permissions](#)

13. Augmenting Branch Predictor to Secure Program Execution

Shi, Yixin; Lee, Gyungho;

[Dependable Systems and Networks. 2007. DSN '07. 37th Annual IEEE/IFIP International Conference](#)
25-28 June 2007 Page(s):10 - 19

Digital Object Identifier 10.1109/DSN.2007.19

[AbstractPlus](#) | Full Text: [PDF\(377 KB\)](#) IEEE CNF

[Rights and Permissions](#)

14. Architectural Support for Run-Time Validation of Control Flow Transfer

Shi, Yixin; Dempsey, Sean; Lee, Gyungho;

[Computer Design. 2006. ICCD 2006. International Conference on](#)
1-4 Oct. 2007 Page(s):506 - 513

Digital Object Identifier 10.1109/ICCD.2006.4380863

[AbstractPlus](#) | Full Text: [PDF\(416 KB\)](#) IEEE CNF

[Rights and Permissions](#)

15. An X86 microprocessor with multimedia extensions

Draper, D.A.; Crowley, M.P.; Holst, J.; Favor, G.; Schoy, A.; Ben-Meir, A.; Trull, J.; Khanna, R.; W Partovi, H.; Johnson, M.; Lee, T.; Mallick, D.; Frydel, G.; Vuong, A.; Yu, S.; Maley, R.; Kauffmann, [Solid-State Circuits Conference. 1997. Digest of Technical Papers. 44th ISSCC. 1997 IEEE Inter](#)

6-8 Feb. 1997 Page(s):172 - 173, 450

Digital Object Identifier 10.1109/ISSCC.1997.585321

[AbstractPlus](#) | Full Text: [PDF\(872 KB\)](#) IEEE CNF

[Rights and Permissions](#)

16. Instruction cache prefetching with extended BTB

Shuh-An Chi; R-Ming Shiu; Jih-Chang Chiu; Si-En Chang; Chung-Ping Chung;

[Parallel and Distributed Systems. 1997. Proceedings.. 1997 International Conference on](#)
10-13 Dec. 1997 Page(s):360 - 365

Digital Object Identifier 10.1109/ICPADS.1997.652574

[AbstractPlus](#) | Full Text: [PDF\(564 KB\)](#) IEEE CNF

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